

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**Patent Application**

Applicant(s): M. Kapur et al.  
Docket No.: YOR920030306US1  
Serial No.: 10/650,222  
Filing Date: August 28, 2003  
Group: 2117  
Examiner: Cynthia H. Britt  
  
Title: Self-Synchronizing Pseudorandom  
Bit Sequence Checker

---

**AMENDMENT AND RESPONSE TO NON-FINAL OFFICE ACTION**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the non-final Office Action dated November 27, 2007, Applicants traverse the various rejections and amend the present application as follows: